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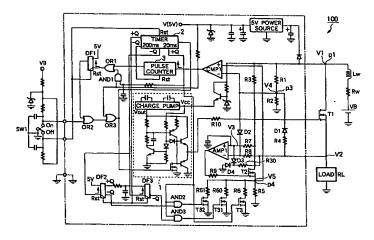
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(54) Title: METHOD OF PROTECTING SEMICONDUCTOR DEVICE AND PROTECTION APPARATUS FOR SEMICON-DUCTOR DEVICE USING THE SAME



(57) Abstract: A protection apparatus for a semiconductor device includes a DC power source (VB), a load/RL), a semiconductor device arranged between the DC power source and the load and switches the load between a driving state and a stopping state, a comparator (MP1) comparing a voltage drop across the semiconductor device with a predetermined reference voltage, and a cut off unit cutting a conduction of the semiconductor device between the DC power source and the load when the voltage drop is greater than the predetermined reference voltage. A constant of the circuit element is set so that the reference voltage is not greater than a critical voltage. The critical voltage is a product of the on-resistance of the semiconductor device when its channel temperature is at an upper limit of the permissible temperature, and a minimum current value which causes the channel temperature to reach the upper limit of the permissible temperature by the self-heating due to Joule heat.

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According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols) IPC 7 + 0.03K + 0.01K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

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X	EP 1 115 203 A (MATSUSHITA ELECTRIC IND COLTD) 11 July 2001 (2001-07-11) paragraph '0056! - paragraph '0072!; figure 1	1-4
X	US 6 335 577 B1 (BABA AKIRA) 1 January 2002 (2002-01-01) column 7, line 20 - column 8, line 41; figures 2,3	1-4
X,P	WO 02/097940 A (YAZAKI CORP; OHSHIMA SHUNZOU (JP)) 5 December 2002 (2002-12-05) page 4, line 7 - page 21, line 15; figures 1-4	1-4

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Date of the actual completion of the international search	Date of mailing of the international search report
25 February 2004	04/03/2004
Name and mailing address of the ISA European Patent Office, P.B. 5818 Patentlaan 2 NL – 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl, Fax: (+31-70) 340-3016	Authorized officer Cantarelli, R

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C.(Continu	ation) DOCUMENTS CONSIDERED TO BE RELEVANT	
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